

This listing of claims replaces all prior versions and listings of claims in the

6 application:

- 53C4> 1. (Currently Amended) Apparatus, comprising:
- an input circuit for receiving a video signal indicative of playback of a first recorded medium;
- 12 a detection circuit for detecting a horizontal synchronization signal component of said video signal, said detection circuit including a horizontal lock detector for determining whether said horizontal synchronization signal is received at appropriate intervals, said horizontal lock detector producing an error signal indicative of the absence of a valid horizontal synchronization signal; and
- a control circuit for controlling pauses in recording of video information from said video signal responsive to detecting said horizontal synchronizing signal.
- 18 2. Cancelled.
3. (Original) The apparatus of claim 2 1, wherein said detection circuit further comprises an error counter, for counting the number of horizontal synchronization signal errors during a predefined time period and indicating whether a threshold level of
- 24 horizontal synchronization signal errors has been exceeded.
4. (Original) The apparatus of claim 3, wherein said horizontal lock detector is responsive to a clocking signal.
5. (Original) The apparatus of claim 2 1, further comprising a vertical lock detector, for
- 30 determining whether a vertical synchronization signal is received at an appropriate time interval.
6. (Original) The apparatus of claim 5, wherein said detection circuit further comprises a countdown timer, responsive to a received horizontal synchronization signal, for producing an output signal indicative of a time interval during which a vertical
- 36 synchronization signal is expected.

7. (Original) The apparatus of claim 6, wherein said detection circuit further comprises
6 a second error counter, for counting the number of vertical synchronization signal errors
during a predefined time period and indicating whether a vertical horizontal
synchronization signal error threshold has been exceeded.

8. (Original) The apparatus of claim 1, wherein:

12 said detection circuit comprises a SYNC separator, for separating at least
horizontal synchronization signals from said video signal; and

a horizontal lock detector for determining whether a horizontal synchronization
signal is received at appropriate intervals, said horizontal lock detector producing an
error signal indicative of the absence of a valid horizontal synchronization signal.

9. (Original) A system, comprising:

18 a first video playback device providing a signal indicative of video recorded on a
first medium during playback of said video; and

24 B₁ a second video playback device responsive to said signal for selectively recording
playback of said video on a second medium with pauses in said recording during at least
one of an absence of video and playback of unacceptable video by said first video
playback device.

10. (Original) The system of claim 9, wherein unacceptable video received by said
second video playback device has associated with it synchronizing signals exhibiting at
least one of degraded wave shapes and improper synchronizing intervals.

11. (Original) The system of claim 10, wherein said synchronizing signals associated
30 with said playback video are evaluated by said second playback device.

12. (Original) The system of claim 11, wherein said second video playback device
comprises:

an input circuit for receiving said playback video;

36 a detection circuit for detecting a horizontal synchronization signal component of
said playback video; and

a control circuit for controlling pauses in recording of video information from said

6 playback video responsive to detecting said horizontal synchronizing signal.

13. (Original) The system of claim 12, wherein said detection circuit comprises a horizontal lock detector for determining whether a horizontal synchronization signal is received at appropriate intervals, said horizontal lock detector producing an error signal indicative of the absence of a valid horizontal synchronization signal.

12 14. (Original) The system of claim 13, wherein said detection circuit further comprises an error counter, for counting the number of horizontal synchronization signal errors during a predefined time period and indicating whether a threshold level of horizontal synchronization signal errors has been exceeded.

18 15. (Original) The system of claim 14, wherein said horizontal lock detector is responsive to a clocking signal.

16. (Original) The system of claim 13, further comprising a vertical lock detector, for determining whether a vertical synchronization signal is received at an appropriate time interval.

24 17. (Original) The system of claim 16, wherein said detection circuit further comprises a countdown timer, responsive to said horizontal synchronization signal, for producing an output signal indicative of a time interval during which a vertical synchronization signal is expected.

30 18. (Original) The system of claim 17, wherein said detection circuit further comprises a second error counter, for counting the number of vertical synchronization signal errors during a predefined time period and indicating whether a vertical horizontal synchronization signal error threshold has been exceeded.

19. (Original) The system of claim 9, wherein:

36 said detection circuit comprises a SYNC separator, for separating at least horizontal synchronization signals from said video signal; and
a horizontal lock detector for determining whether a horizontal synchronization

signal is received at appropriate intervals, said horizontal lock detector producing an
6 error signal indicative of the absence of a valid horizontal synchronization signal.

20. (Currently Amended) A method for controlling a recording device, comprising the
steps of:

evaluating synchronizing components associated with a received video signal to
determine if said received video signal provides acceptable video;

12 separating said synchronization components from said received video signal;
determining whether a horizontal synchronization signal is received at appropriate
intervals; and

B.
C. providing indicium of the absence of a valid horizontal synchronization signal;
enabling said recording device to record said received video in response to a
positive evaluation of said synchronizing components; and

18 causing said recording device to enter a pause mode of operation in response to
a negative evaluation of said synchronizing components associated with said received
video.

21. (Original) The method of claim 20, further comprising the step of:

separating said synchronization components from said received video signal.

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22. Cancelled.
